REMARKS

Claims 1–4 stand rejected under 35 U.S.C. 103(a) as being unpatentalbe over Asami et al., U.S. Patent No. 6,036,100, (hereinafter "Asami") in view of Commericial (WO99/23550). New claims 5-6 have been added, support for new claims 5-6 can be found in the specification at least on page 2, lines 4-23. Claims 1-4 have been amended to more particularly claim the invention. Claims 1-6 are in the application. Applicants respectfully submit that the pending claims, as amended, are patentable for at least the following reasons.

Claim 1 is directed to A data carrier comprising a data processing unit, wherein the data processing unit being constructed at least mainly of at least substantially asynchronously operating logic components, and at least one contactless interface, wherein the data processing unit can be coupled to a read/write apparatus in order to exchange data signals and to take up electrical energy for operation of the data processing unit, wherein selected asynchronously operating logic components are activated in response to a request signal in a coordinated manner.

Asami, as read by the applicants, relates to a noncontact IC which transmits and receives data to and from a host computer using RF signals has a buffer for storing received data temporarily and a control circuit for controlling operation of the buffer main memory thereof, wherein the control circuit starts processing data stored in the buffer only when no further data is input after a

predetermined data receiving time period has elapsed from the latest data input to the buffer.

Commericial (WO99/23550), as read by the applicants, relates to a microprocessor comprising a means for concatenating bits

Asami and Commericial (WO99/23550), fail, either alone or in combination, to teach, show or disclose that selected asynchronously operating logic components are activated in response to a request signal in a coordinated manner, as specifically recited in amended independent claim 1.

Since Asami and Commericial (WO99/23550), does not teach, show or suggest all of the features of amended independent claim 1, as recited above, applicant respectfully submits that this claim is patentable over this art.

A review of the other art of record has failed to reveal anything which, in Applicants' opinion, would remedy the deficiencies of the art discussed above, as a reference against the independent claims herein. These claims are therefore believed patentable over the art of record.

The other claims in this application are each dependent from the independent claim discussed above and are therefore believed patentable for the same reasons. Since each dependent claim is also deemed to define an additional aspect of the invention, however, the individual consideration of the patentability of each on its own merits is respectfully requested.

The applicants submit that the claims fully satisfy the requirements of 35 U.S.C. 103. In view of the foregoing remarks, favorable reconsideration and early passage to issue of the present application are respectfully solicited.

Applicants' undersigned attorney may be reached by telephone at the number given below.

Respectfully submitted,

By _____

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VERSION WITH MARKING TO SHOW CHANGES MADE

Please amend the claims as follows:

1. (Amended) A data carrier~~, notably a chip card which includes~~ comprising:

_____-a data processing unit, wherein the data processing unit being constructed at least mainly of at least substantially asynchronously operating logic components; and

_____at least one contactless interface, wherein ~~via which~~ the data processing unit can be coupled to a read/write apparatus in order to exchange data signals and to take up electrical energy for operation of the data processing unit, ~~the data processing unit being constructed at least mainly of at least substantially asynchronously operating logic components (asynchronous logic)~~

_____wherein selected asynchronously operating logic components are activated in response to a request signal in a coordinated manner.

2. (Amended) ~~A~~ The data carrier as claimed in Claim 1, ~~characterized in that~~wherein the contactless interface and the data processing unit are coupled to one another via an asynchronous transmission/receiving circuit which is included in the data processing unit.

3. (Amended) The~~A~~ data carrier as claimed in Claim 1, ~~characterized in that~~wherein individual stages within at least the data processing unit operate in a time interleaved manner.

4. (Amended) A~~The~~ data carrier as claimed in Claim 1, ~~characterized in that~~wherein the contactless interface for the electrical energy for the operation of

the data processing unit has the function of an at least substantially ideal current source.

5. (New) The data carrier as claimed in Claim 1, wherein the coordinated manner includes an activated selected asynchronously operating logic component providing a finished message after executing its operation, the finished message operable as a request message to another selected asynchronously operating logic component.

6. (New) The data carrier as claimed in Claim 1, wherein the coordinated manner includes the propagation of a request message from a first selected asynchronously operating logic component to a second selected asynchronously operating logic component in a series of operating steps.